PNP/NPN Epitaxial Planar Silicon Transistor



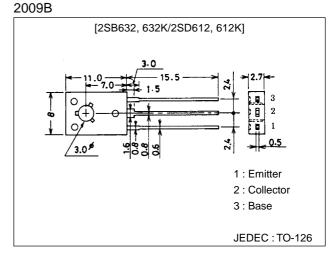
2SB632, 632K/2SD612, 612K 25V/35V, 2A Low-Frequency Power Amplifier Applications

Features

· High collector dissipation and wide ASO.

Package Dimensions

unit:mm



(): 2SB632, 632K

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	2SB632, D612	2SB632K, D612K	Unit	
Collector-to-Base Voltage	VCBO		(–)25	(–)35	V	
Collector-to-Emitter Voltage	VCEO		(–)25	(–)35	V	
Emitter-to-Base Voltage	VEBO			(-)5		
Collector Current	IC			(–)2		
Collector Current (Pulse)	ICP			(-)3		
Collector Dissipation	PC			1		
		Tc=25°C		10	W	
Junction Temperature	Tj			150		
Storage Temperature	Tstg			–55 to +150		

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Ratings			Unit
				min	typ	max	Unit
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =(-)10μA, I _E =0	B632, D612	(–)25			V
			B632K, D612K	(–)35			V
Collector-to-Emitter Brakdown Voltage	V(BR)CEO	I _C =(−)1mA, R _{BE} =∞	B632, D612	(–)25			V
			B632K, D612K	(–)35			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =(–)10μΑ, I _C =0		(–)5			V
Collector Cutoff Current	ICBO	V _{CB} =(-)20V, I _E =0				(–)1	μA
Emitter Cutoff Current	IEBO	V _{EB} =(-)4V, I _C =0				(–)1	μA
* : The 2SB632/2SD612 are classified by 500m.	A here as follo	WS: 60 D 120 100	E 200 160 E	220			

* : The 2SB632/2SD612 are classified by 500mA h_{FE} as follows : 60 D 120 100 E 200 160 F 320

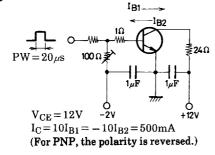
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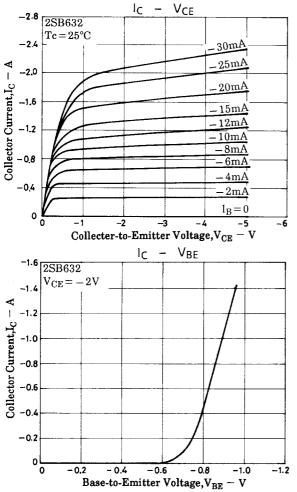
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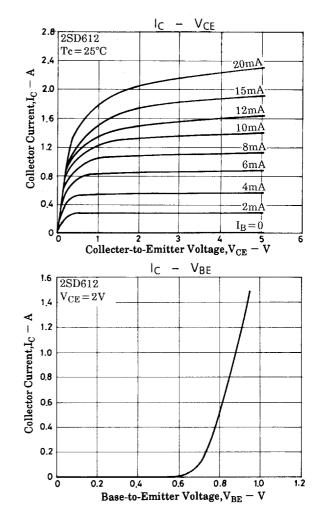
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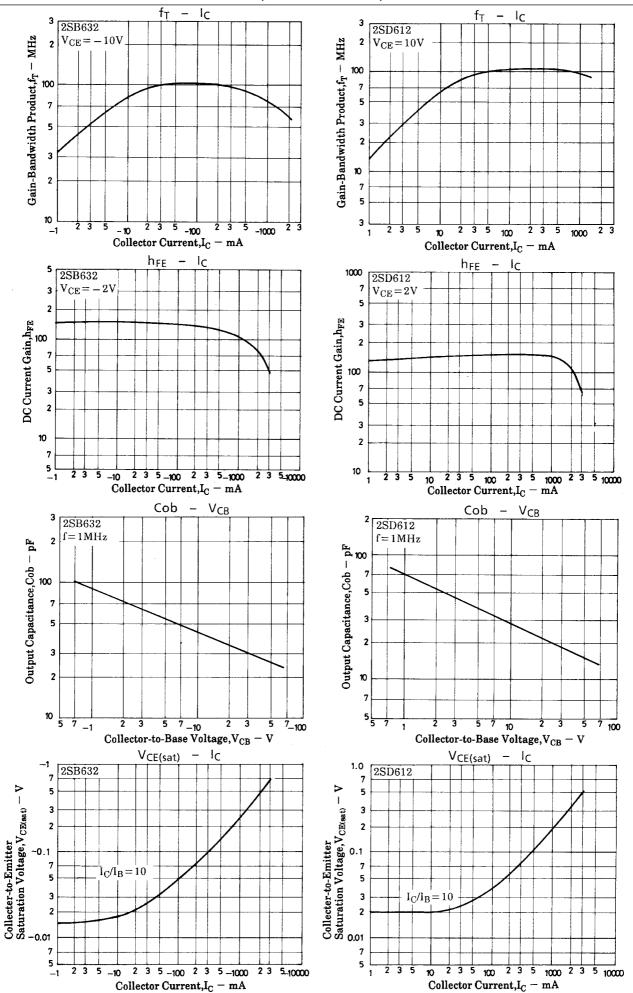
Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
DC Current Gain	h _{FE} 1	V _{CE} =(-)2V, I _C =(-)500mA	60*		320*	
	h _{FE} 2	V _{CE} =(-)2V, I _C =(-)1.5A	30			
Gain-Bandwidth Product	fT	V _{CE} =(-)10V, I _C =(-)50mA		100		MHz
Output Capacitance	Cob	V _{CB} =(-)10V, f=1MHz		(45)30		pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =(-)1.5A, I _B =(-)0.15A		(-0.4)	(-0.9)	V
				0.3	0.8	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)1.5A, I _B =(-)0.15A		(–)1.1	(–)1.5	V
Turn-ON Time	ton	See specified Test Circuit		(60)50		ns
Fall Time	t _f	See specified Test Circuit		(80)		ns
				100		ns
Storage Time	t _{stg}	See specified Test Circuit		400		ns

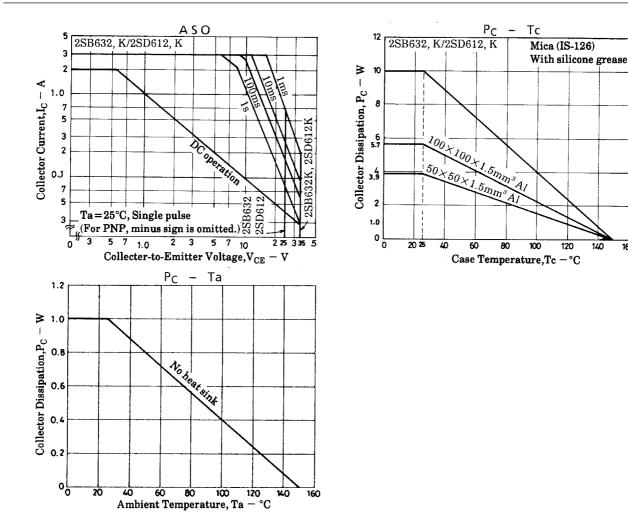
Switching Time Test Circuit







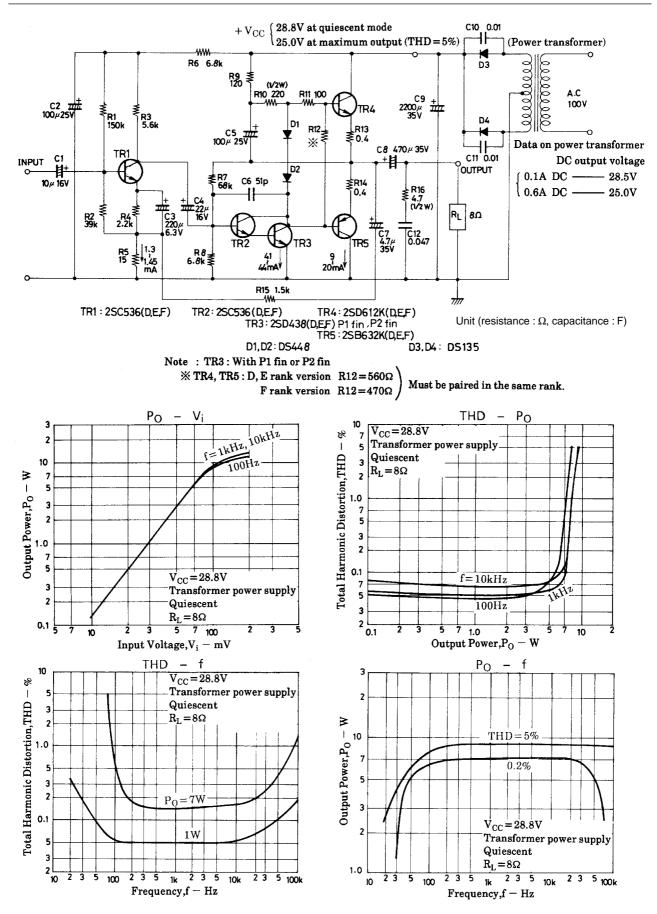


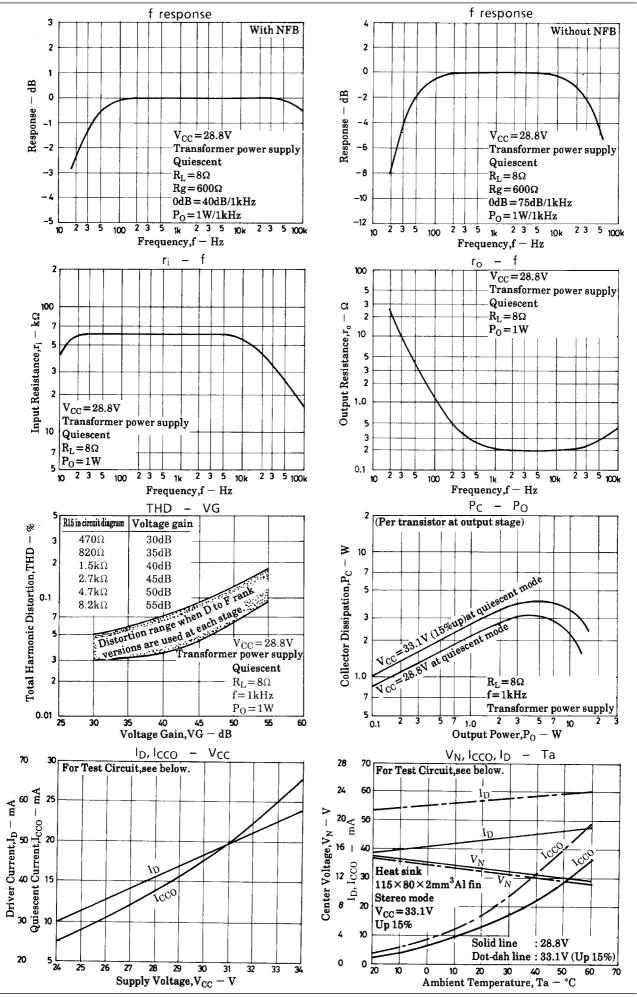


Sample Application Circuit 1:8W pure complementary amplifier using the 2SB632K/2SD612K [Specifications] Power supply : 100V AC supply transformer with no signal=28.8V. Maximum output=(THD=5%)=25V, f=1kHz, $R_L=8\Omega$, $R_g=600\Omega$.

Parameter	Symbol	Conditions	typ	Unit
Quiescent Current (Collector Current)	Icco	Output stage	14.0	mA
	I _D	Drive stage	42.0	mA
	IC	First stage	1.4	mA
Voltage Gain	VG	Without NFB	75	dB
	VG	With NFB	40	dB
Output Power	PO	THD=5%	8.7	W
Total Harmonic Distortion	THD	P _O =1W	0.05	%
Input Resistance	rj	P _O =1W	60	kΩ
Output Resistance	r _o	P _O =1W	0.2	Ω

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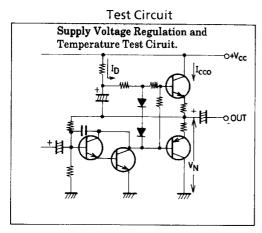


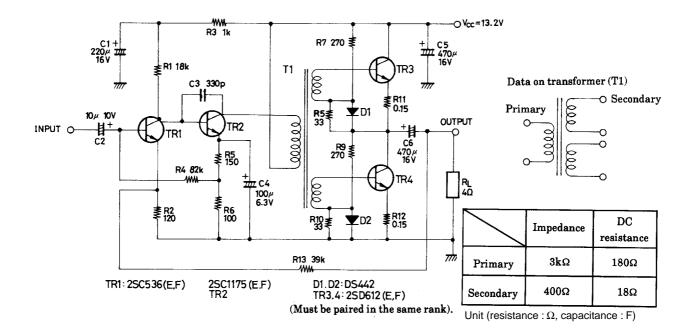
Sample Application Circuit 2 : 2SD612-Used

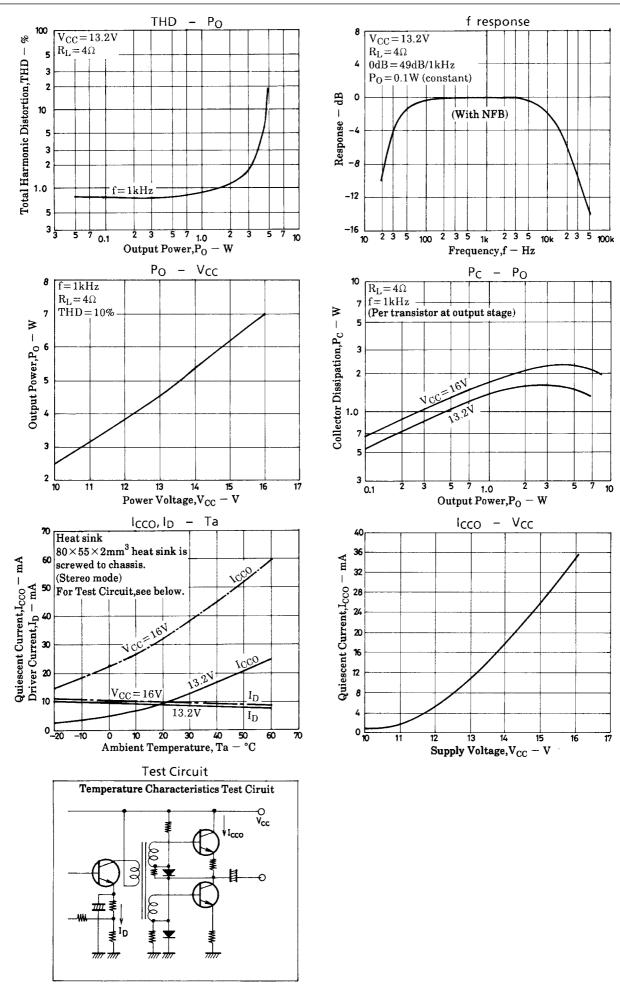
4W Input Transtformer coupling Amplifier for Car Use.

[Specifications] V_{CC} =13.2V, R_L =4 Ω , R_g =600 Ω , f=1kHz.

Parameter	Symbol	Conditions	typ	Unit
Quiescent Current (Collector Current) Voltage Gain	lcco	Output stage	12.0	mA
	I _D	Drive stage	9.0	mA
Voltage Gain	VG	Without NFB	66	dB
	VG	With NFB	49	dB
Output Power	PO	THD=10%	4.7	W
Total Harmonic Distortion	THD	P _O =0.5W	0.8	%
Input Impedance	ri	P _O =0.5W	60	kΩ







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